

# Digital Logic Circuit Analysis And Design

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Digital Logic Circuit Analysis and Design (second Edition)  
Digital Circuit Analysis and Design with Simulink Modeling and Introduction to CPLDs and FPGAs  
Logic circuit analysis program (Icap)  
Computer Analysis of a Digital Logic Circuit  
Digital Logic and Switching Circuits Design, Analysis and Test of Logic Circuits Under Uncertainty  
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Logic Circuit Design Computer Circuit Analysis  
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Fluid Power Logic Circuit Design Auburn Logic Circuit Analysis Package (ALCAP)  
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Ternary Sequential Logic Circuit Analysis  
Victor Nelson Victor Peter Nelson Victor Peter Nelson Victor P Nelson Victor Peter Nelson Steven T. Karris S. Alpert Rodney Jerome Means Jefferson C. Boyce Smita Krishnaswamy  
Community College of the Air Force (U.S.)  
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the book is an undergraduate level textbook presenting a thorough discussion of state of the art digital devices and circuits it is self contained

logic circuits are becoming increasingly susceptible to probabilistic behavior caused by external radiation and process variation in addition inherently probabilistic quantum and nano technologies are on the horizon as we approach the limits of cmos scaling ensuring the reliability of such circuits despite the probabilistic behavior is a key challenge in ic design one that necessitates a fundamental probabilistic reformulation of synthesis and testing techniques this monograph will present techniques for analyzing designing and testing logic circuits with probabilistic behavior

in three main divisions the book covers combinational circuits latches and asynchronous sequential circuits combinational circuits have no memorising ability while sequential circuits have such an ability to various degrees latches are the simplest sequential circuits ones with the shortest memory the presentation is decidedly non standard the design of combinational circuits is discussed in an orthodox manner using normal forms and in an unorthodox manner using set theoretical evaluation formulas relying heavily on karnaugh maps the latter approach allows for a new design technique called composition latches are covered very extensively their memory functions are expressed mathematically in a time independent manner allowing the use of normal non temporal boolean logic in their calculation the theory of latches is then used as the basis for calculating asynchronous circuits asynchronous circuits are specified in a tree representation each internal node of the tree representing an internal latch of the circuit the latches specified by the tree itself the tree specification allows solutions of formidable problems such as algorithmic state assignment finding equivalent states non recursively and verifying asynchronous circuits

these two volumes constitute the revised selected papers of the 5th international

conference csei 2023 held in kunming china during august 11 13 2023 the 76 full papers and the 21 short papers included in this volume were carefully reviewed and selected from 297 submissions they focus on computer science education informatization and engineering education innovative application for the deeper integration of education practice and information technology educational informatization and big data for education

this book is an undergraduate level textbook presenting a thorough discussion of state of the art digital devices and circuits it supplements our electronic devices and amplifier circuits isbn 0 9744239 4 7 it is self contained begins with the basics and ends with the latest developments of the digital technology the intent is to prepare the reader for advanced digital circuit design and programming the powerful complex programmable logic devices cplds and field programmable gate arrays fpgas the prerequisites for this text are just basic high school math accordingly it can be read and understood by high school seniors trade school community college and 4 year university students it is ideal for self study chapter 1 is an introduction to the decimal binary octal and hexadecimal numbers their representation and conversion from one base to another chapter 2 presents an introduction to arithmetic operations in binary octal and hexadecimal numbers the tens complement and nines complements in the decimal system and the twos complement and ones complements in the binary system are discussed and illustrated with numerous examples chapter 3 begins with an introduction to sign magnitude representation of binary numbers it concludes with a discussion on floating point arithmetic for representing large numbers and the ieee standard that specifies single precision 32 bit and double precision 64 bit floating point representation of numbers chapter 4 describes the most commonly used binary codes the binary coded decimal bcd the excess 3 code the 2 421 code the gray code and the american standard code for information interchange ascii code are introduced as well as the use of parity bits chapter 5 begins with the basic logic operations and continues with the fundamentals of boolean algebra and the basic postulates and theorems as applied to electronic logic circuits truth tables are defined and examples are given to illustrate how they can be used to prove boolean algebra theorems or equivalent logical expressions chapter 6 introduces the standard forms of expressing boolean functions the minterms and maxterms also known as standard products and standard sums respectively a procedure is also presented to show how one can convert one form to the other this topic is essential in understanding the programming of programmable logic arrays plus discussed in chapter 11 chapter 7 is an introduction to combinational logic circuits

it begins with methods of implementing logic diagrams from boolean expressions the derivation of boolean expressions from logic diagrams input and output waveforms and the use of karnaugh maps for simplifying boolean expressions chapter 8 is an introduction to sequential logic circuits it begins with a discussion of the different types of flip flops and continues with the analysis and design of binary counters registers ring counters and ring oscillators chapter 9 is an introduction to computer memory devices we discuss the random access memory ram read only memory rom row and column decoders memory chip organization static rams srams dynamic rams drams volatile nonvolatile programmable roms proms erasable proms eproms electrically erasable proms eeproms flash memories and cache memory chapter 10 begins with an introduction to the basic components of a digital computer it continues with a discussion of the basic microprocessor operations and concludes with the description of more advanced arithmetic and logic operations we consider chapter 11 as the highlight of this text it is an introduction to field programmable devices fpgas also referred to as programmable logic devices pld's it begins with the description and applications of programmable logic arrays plas continues with the description of simple pld's splds and complex pld's cplds and concludes with the description of field programmable gate arrays fpgas this text includes also four appendices appendix a is an overview of the advanced boolean equation language abel which is an industry standard hardware description language hdl used in programmable logic devices pld's appendix b describes the vhsc hardware description language briefly referred to as vhdl this language was developed to be used for documentation verification and synthesis of large digital designs appendix c introduces the verilog hardware description language hdl like vhdl introduced in appendix b verilog is a programming language used to describe a digital system and its components appendix d is a brief discussion on the boundary scan architecture and the new technology trends that make using boundary scan essential for the reduction in development and production costs

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